

Suzuha - operating system for

ARM Cortex M3, M4

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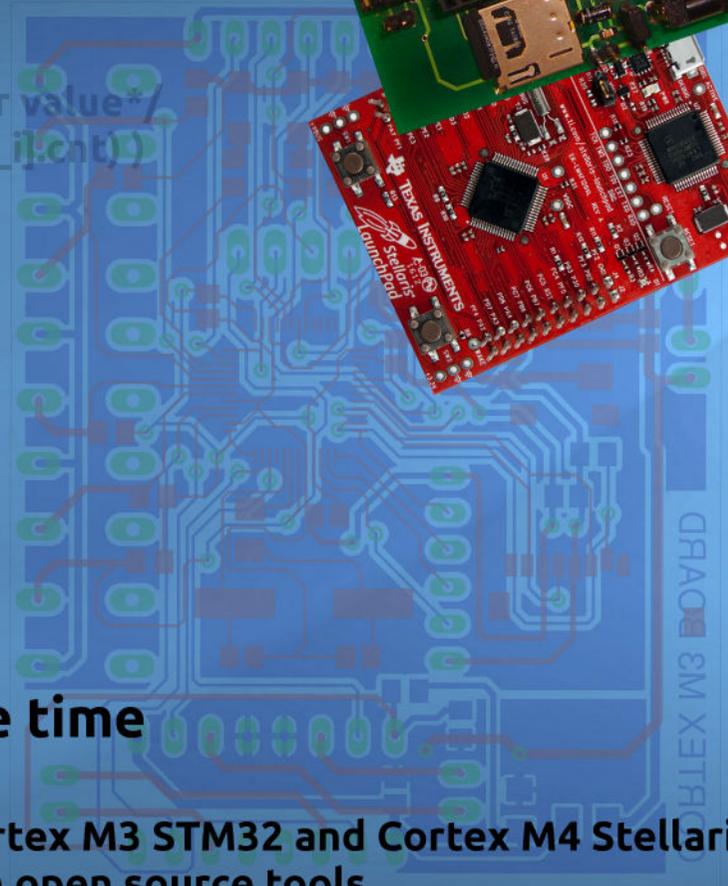
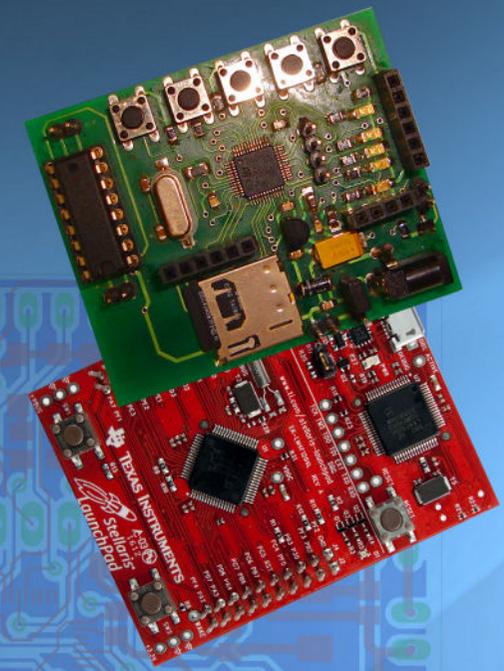
void scheduler()
{
  #if SCHED_PRIORITY==1
  /*death times priority round robin*/
  u32 i, min_i=__current_task__;

  for (i=0; i<TASK_MAX_COUNT; i++) /*find task with minimum counter value*/
  if ( ((__task__[i].flag&TF_CREATED)!=0) && (__task__[i].cnt<__task__[min_i].cnt) )
  min_i=i;
  * preemptive multitasking
  * high modularity
  * priority scheduling
  for (i=0; i<TASK_MAX_COUNT; i++) /*decrement counters*/
  if ( __task__[i].cnt-- )
  __task__[min_i].cnt=__task__[min_i].icnt; /*set initial counter value*/
  __current_task__=min_i;

  #else
  * microkernel
  * small size - 5kB

  /*round robin*/
  do /*find next task in cycle*/
  {
    * locks - mutexes
    * messages
    * cli and file system
    __current_task__++;
    if ( __current_task__>=TASK_MAX_COUNT )
    __current_task__=0;
  }
  while ( ( (__task__[__current_task__].flag&TF_CREATED)==0) );
  #endif
}

```



- * short response time
- * portability for Cortex M3 STM32 and Cortex M4 Stellaris
- * development with open source tools

